

## Registration Guidelines for Students

### Contact to your respective Batch Advisor for completing course registration.

1. Bring Registration Clearance and Find the specific time of your Batch Advisor before come to registration.
2. Find your "Batch Advisor" to register the courses. Do not attempt for other Teachers.
3. Register your offered courses. Do not go for extra/advance courses.
4. You are advised to register backlog courses (like "F" grade, "I" grade and "W" grade) first then regular offered courses.
05. A student can take a course as retake if its grade be **below B+**.
06. Do not take any courses if pre-requisite courses were not completed.
07. Write the appropriate (as per the course offer) courses name and course Code in the registration Clearance slip and give it to the Batch Advisor for Registration.
08. Old syllabus students (before 191 initial id students) are advised to check course code, title, credit and section of the individual course before completing their registration.
09. Optional courses are batch specific. Check your optional course before Register.
10. Come on that particular day selected for you by the authority.
11. Join in the first day of class starts.
12. We will maintain the section size(50) strictly, therefore you advised to register your courses within the scheduled date.



**Faculty of Science and Information Technology  
Department of Computer Science  
and Engineering NOTICE for the  
Registration of Fall-2023  
Semester**

It is hereby notified to all the students of Dept. of Computer Science and Engineering that their course registration for **Fall-**

**2023 Semester** will be held according to the following schedule. The Course Advisor will help the students in the registration process. Without the course registration, students will not be allowed in the class and their name and ID will not be recorded in the attendance register.

**Registration Date: 2<sup>nd</sup>, 3<sup>rd</sup>, 4<sup>th</sup>, 5<sup>th</sup>, 6<sup>th</sup>, 8<sup>th</sup>, 9<sup>th</sup>, 10<sup>th</sup> and 11<sup>th</sup> July 2023.**

**Time: 9:00am-1:00pm and 2:00pm – 4:00pm**

**Contact your respective Batch Advisor for completing your Course Registration. Retake subjects (GED) should be registered in Retake Section if it is available.**

**Old Syllabus students are asked to register in the Old Syllabus Course's section (if it is available).**

**Steps to be followed-**

1. All the students are suggested to collect the Registration Clearance Form from the Accounts Section by paying **1<sup>st</sup> installment money** to DIU accounts section, detail schedule of collection already mailed to you and posted on Online Notice Board.
2. Students are also suggested to clear all the previous dues before the date of the registration.
3. Students are advised to follow the same procedure like previous semester to confirm their registration.
4. **Students must follow their respective syllabus / course structure (Course code/title) which is available in department notice board/ online notice board/ Dept. Facebook page and Digital Notice Board.**

**Classes of Fall-2023 semester will start from 12<sup>th</sup> July, 2023.**

**No Add/Drop will be allowed after the last date of Registration. Retake subject (GED) should be registered in Retake Section (if it is available).**

**Professor Dr. Sheak Rashed Haider Noori**

Head (In-charge)

Department of CSE

Daffodil International University



**Department of Computer Science and Engineering**  
**Course Offer for Fall 2023**

Code	L-1, T-1 (Batch-65) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
ENG101	Basic Functional English and English Spoken	Null	3
MAT101	Mathematics - I	Null	3
PHY101	Physics-I	Null	3
CSE112	Computer Fundamentals	Null	3
CSE-113	Programming and Problem Solving	Null	3
CSE-114	Programming and Problem Solving Lab	Null	1.5
CSE-115	Introduction to Biology and Chemistry for Computation	Null	3
		<b>Total Credits</b>	<b>19.5</b>

Code	L-1, T-2 (Batch-64) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
ENG102	Writing and Comprehension	ENG-101	3
MAT102	Mathematics-II: Calculus, Complex Variables and Linear Algebra	MAT-101	3
PHY 102	Physics - II	PHY-101	3
PHY 103	Physics - II Lab	PHY-101	1.5
CSE121	Electrical Circuits	PHY-101	3
CSE122	Electrical Circuits Lab	PHY-101	1.5
CSE-123	Data Structure	CSE-113	3
CSE-124	Data Structure Lab	CSE-114	1.5
		<b>Total Credits</b>	<b>19.5</b>

Code	L-2, T-1 (Batch-63) (New Curriculum-2, Effective from Spring 2022)	Pre-requisite	Credit
MAT211	Engineering Mathematics	MAT121	3
CSE212	Basic Electronics	CSE132	1
CSE213	Basic Electronics Lab	CSE133	2
CSE214	Object Oriented Programming	CSE122	3
CSE215	Object Oriented Programming Lab	CSE123	1
GED216	History of Bangladesh and Bangla Language	Null	3
ACT322	Financial and Managerial Accounting	Null	2
ECO237	Economics	Null	3
CSE226	Software Project I	Null	1
		<b>Total Credits</b>	<b>19</b>

Code	L-2, T-2 (Batch-62) (New Curriculum-2, Effective from Spring 2022)	Pre-requisite	Credit
CSE214	Object Oriented Programming	CSE122	3
CSE215	Object Oriented Programming Lab	CSE123	1
CSE223	Digital Electronics	CSE132	1
CSE224	Digital Electronics Lab	CSE133	2
CSE225	Data Communication	CSE212	3
STA227	Statistics and Probability	MAT111	3
GED216	History of Bangladesh and Bangla Language	Null	3
CSE226	Software Project I	Null	1

		<b>Total Credits</b>	<b>17</b>
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<b>Code</b>	<b>L-2, T-2 (Batch-61) (New Curriculum-2, Effective from Spring 2022)</b>	<b>Pre-requisite</b>	<b>Credit</b>
CSE231	Algorithm	CSE134	3
CSE232	Algorithm Lab	CSE135	1
CSE134	Data Structure	CSE122	3
CSE135	Data Structure Lab	CSE123	1
STA227	Statistics and Probability	MAT111	3
CSE235	Numerical Methods	MAT121, MAT211	3
ECO237	Economics	Null	3
CSE226	Software Project I	Null	1
CSE236	Software Project II	Null	1
		<b>Total Credits</b>	<b>19</b>

<b>Code</b>	<b>L-3, T-1 (Batch-60) (New Curriculum-1, Effective from Spring 2019)</b>	<b>Pre-requisite</b>	<b>Credit</b>
CSE315	Artificial Intelligence	CSE213, CSE 214	1
CSE316	Artificial Intelligence Lab	CSE213, CSE 214	2
CSE231	Microprocessor, Embedded Systems and IoT	CSE132,223	1
CSE232	Microprocessor, Embedded Systems and IoT Lab	CSE133,224	2
CSE311	Database Management System	CSE 134, CSE 221	2
CSE312	Database Management System Lab	CSE 135, CSE 222	2
CSE313	Computer Networks	CSE233	1
CSE314	Computer Networks Lab	CSE234	2
CSE236	Math for Computer Science	Null	2
CSE237	Software Project IV	Null	1
		<b>Total Credits</b>	<b>16</b>

<b>Code</b>	<b>L-3, T-2 (Batch-59) (New Curriculum-1, Effective from Spring 2019)</b>	<b>Pre-requisite</b>	<b>Credit</b>
CSE315	Artificial Intelligence	CSE213, CSE 214	1
CSE316	Artificial Intelligence Lab	CSE213, CSE 214	2
CSE321	Data Mining and Machine Learning	CSE213, CSE 214	2
CSE322	Data Mining and Machine Learning Lab	CSE213, CSE 214	2
CSE323	Operating Systems	CSE213,CSE214	1
CSE324	Operating Systems Lab	CSE213,CSE214	2
CSE325	System Analysis and Design	CSE214	3
ECO321	Economics	Null	2
CSE326	Research and Innovation	Null	1
CSE317	Software Project V	Null	1
		<b>Total Credits</b>	<b>17</b>

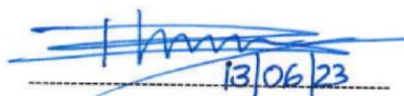
<b>Code</b>	<b>L-3, T-2 (Batch-58) (New Curriculum-1, Effective from Spring 2019)</b>	<b>Pre-requisite</b>	<b>Credit</b>
CSE321	Data Mining and Machine Learning	CSE213, CSE 214	2
CSE322	Data Mining and Machine Learning Lab	CSE213, CSE 214	2

CSE331	Complier Design	CSE213, CSE 214	2
CSE332	Complier Design Lab	CSE213, CSE 214	2
CSE334	Pervasive Computing	CSE214	1
CSE335	Pervasive Computing and Mobile App Development Lab	CSE214	2
CSE325	System Analysis and Design	CSE214	3
CSE333	Software Engineering	CSE214	3
CSE326	Research and Innovation	Null	1
CSE336	Software Project VI	Null	1
		<b>Total Credits</b>	<b>19</b>

Code	L-4, T-1 (Batch-57) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE412	Big Data and IoT	CSE231	1
CSE413	Big Data and IoT Lab	CSE232	2
CSE414	Web Engineering	CSE 214	1
CSE415	Web Engineering Lab	CSE 214	2
CSE421	Computer Graphics	MAT211, CSE 213, CSE222	1
CSE422	Computer Graphics Lab	MAT211, CSE 213, CSE222	2
CSE333	Software Engineering	CSE 214	3
CSE 445	Natural Language Processing [Elective-1]		3
CSE336	Software Project VI	Null	1
CSE499	Project / Internship (Phase I, to be completed in Level-4 Term-3)	Null	2
		<b>Total Credits</b>	<b>18</b>

Code	L-4, T-2 (Batch-56) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE423	Information Security	Null	3
CSE 445	Natural Language Processing [Elective-1]		3
CSE498	Social and Professional Issues in Computing	Null	3
CSE499	Project / Internship (Phase I): (2 Credits)	Null	2
		<b>Total Credits</b>	<b>11</b>

Code	L-4, T-2 (Batch-55) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE412	Big Data and IoT	CSE231	1
CSE413	Big Data and IoT Lab	CSE232	2
CSE426	Principles of Robotics [Elective-2]	CSE231, CSE232	3
CSE427	Digital Image Processing	CSE133>MAT221	3
CSE498	Social and Professional Issues <i>in Computing</i>	Null	3
CSE499	Project / Internship (Phase II): (3 Credits)	Null	3
		<b>Total Credits</b>	<b>15</b>

  
13/06/23

Prof. Dr. Touhid Bhuiyan  
Head  
Department of CSE



**Faculty of Science and Information Technology**  
**Department of Computer Science and Engineering**  
**Evening Course Offer for Fall 2023 Semester**

<b>Batch 57</b>	<b>ID Series: 203_CC</b>		<b>Level-3 Term-2</b>
Course Code	Course Title	Credit	Prerequisite
CSE413	Simulation and Modeling	3	CSE213>MAT223
CSE413L	Simulation and Modeling Lab	1	CSE413
MGT414	Industrial Management	3	Null
CSE331	Compiler Design	3	CSE213>CSE231
CSE331L	Compiler Design Lab	1	CSE331
CSE499	Project/Internship (Phase II)	3	Null
	<b>Total Credits</b>	<b>14</b>	

<b>Batch 57</b>	<b>ID Series: 203_DSC</b>		<b>Level-3 Term-2</b>
Course Code	Course Title	Credit	Prerequisite
CSE231	Microprocessor and Assembly Language	3	CSE132
CSE231L	Microprocessor and Assembly Language Lab	1	CSE224
CSE224	Electronic Devices and Circuits	3	CSE132/CSE212
CSE224L	Electronics Devices and Circuits Lab	1	CSE231
CSE232	Instrumentation & Control	3	CSE224
CSE313	Computer Networks	3	CSE233
CSE313L	Computer Networks Lab	1	CSE313
CSE499	Project/Internship (Phase II)	3	Null
	<b>Total Credits</b>	<b>18</b>	

<b>Batch 58</b>	<b>ID Series: 211_CC</b>		<b>Level-2 Term-2</b>
Course Code	Course Title	Credit	Prerequisite
MAT211	Engineering Mathematics	3	MAT121
CSE411	Communication Engineering	3	CSE313
CSE412	Artificial Intelligence	3	CSE131>CSE213
CSE412L	Artificial Intelligence Lab	1	CSE411
CSE333	Peripherals and Interfacing	3	Null
CSE499	Project / Internship (Phase I)	3	Null
	<b>Total Credits</b>	<b>16</b>	

<b>Batch 58</b>	<b>ID Series: 211_DSC</b>		<b>Level-3 Term-2</b>
Course Code	Course Title	Credit	Prerequisite
CSE131	Discrete Mathematics	3	Null
ENG113	English Language I	3	Null

MAT121	Mathematics - II: Linear Algebra and Coordinate Geometry	3	Null
PHY123	Physics II: Electricity, Magnetism and Modern Physics	3	PHY114
PHY123L	Physics II Laboratory	1	PHY123
CSE231	Microprocessor and Assembly Language	3	CSE132
CSE231L	Microprocessor and Assembly Language Lab	1	CSE224
<b>CSE499</b>	<b>Project / Internship (Phase I)</b>	<b>3</b>	<b>Null</b>
<b>Total Credits</b>		<b>20</b>	

Batch 59		ID Series: 212_DSC_&_CC		Level-2 Term-2
Course Code	Course Title	Credit	Prerequisite	
CSE411	Communication Engineering	3	CSE313	
CSE432	Computer and Network Security	3	Null	
CSE412	Artificial Intelligence	3	CSE131>CSE213	
CSE412L	Artificial Intelligence Lab	1	CSE411	
CSE333	Peripherals and Interfacing	3	Null	
<b>CSE499</b>	<b>Project / Internship (Phase I)</b>	<b>3</b>	<b>Null</b>	
<b>Total Credits</b>		<b>16</b>		

Batch 60		ID Series: 213_CC		Level-2 Term-2
Course Code	Course Title	Credit	Prerequisite	
CSE131	Discrete Mathematics	3	Null	
ENG113	English Language I	3	Null	
MAT121	Mathematics - II: Linear Algebra and Coordinate Geometry	3	Null	
PHY123	Physics II: Electricity, Magnetism and Modern Physics	3	PHY114	
PHY123L	Physics II Laboratory	1	PHY123	
CSE312	Numerical Methods	3	MAT121>MAT211	
CSE331	Compiler Design	3	CSE213>CSE231	
CSE331L	Compiler Design Lab	1	CSE331	
<b>CSE499</b>	<b>Project / Internship (Phase I)</b>	<b>3</b>	<b>Null</b>	
<b>Total Credits</b>		<b>23</b>		

Batch 60		ID Series: 213_DSC		Level-2 Term-1
Course Code	Course Title	Credit	Prerequisite	
MAT223	Statistics	3	Null	
MAT211	Engineering Mathematics	3	MAT121	
CSE323	Operating Systems	3	CSE213>CSE231	
CSE323L	Operating Systems Lab	1	CSE323	
CSE413	Simulation and Modeling	3	CSE213>MAT223	
CSE413L	Simulation and Modeling Lab	1	CSE413	
CSE431	E-Commerce and Web Applications	3	CSE321	
CSE421	Computer Graphics	3	CSE213>MAT121 CSE222>STA223	

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GSE421L	Computer Graphics Lab	1	CSE421
GSE499	Project / Internship (Phase I)	3	Null
	Total Credits	24	

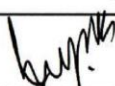
Batch 61	ID Series: 221 DSC	Level-2 Term-0	
Course Code	Course Title	Credit	Prerequisite
GSE311	Database Management System	3	CSE133 > CSE222
GSE311L	Database Management System Lab	1	CSE311
MGT414	Industrial Management	3	Null
GSE331	Compiler Design	3	CSE213 > CSE231
GSE331L	Compiler Design Lab	1	CSE331
GSE313	Computer Networks	3	CSE233
GSE313L	Computer Networks Lab	1	CSE313
GSE431	E-Commerce and Web Applications	3	CSE321
	Total Credits	18	

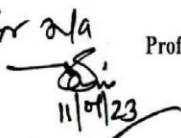
*Haider*  
11.06.2023

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 Professor Dr. Sheik Rashid Haider **ii**pori  
 Head (In-charge)  
 Department of CSE

**Daffodil International University**  
**Academic Calendar of Bi-Semester**  
**For the year 2023**

Descriptions	Spring 2023 (January - June)	Fall 2023 (July - December)
Advising and Registration	January 01-10, 2023	July 02-11, 2023
Classes Start	January 11, 2023	July 12, 2023
First Quiz	2nd week of February, 2023	2nd week of August, 2023
Second Quiz	1st week of March, 2023	1st week of September, 2023
<b>Mid Semester Examination</b>	<b>March 11-23, 2023</b>	<b>October 05-19, 2023</b>
Class starting date after Midterm	March 24, 2023	October 20, 2023
Third Quiz	3rd week of April, 2023	2 <sup>nd</sup> Week of November, 2023
<b>Eid-UI-Fitr Vacation</b>	<b>April 20-24, 2023</b>	-
Final Exam preparation & Review Classes	June 06-09, 2023	December 01-06, 2023
<b>Final Examination</b>	<b>June 10-22, 2023</b>	<b>December 07-21, 2023</b>
<b>Last Date for Submission of Final Grades</b>	<b>June 25, 2023</b>	<b>December 24, 2023</b>
<b>Publication of Semester Results</b>	<b>June 26, 2023</b>	<b>December 27, 2023</b>
<b>Semester Break</b>	<b>June 26, 2023 – July 01, 2023 (Semester Break &amp; Eid-UI-Azha Vacation)</b>	<b>December 28-31, 2023</b>
Advising & Registration for next semester	July 02-11, 2023	January 01-10, 2024
Classes Start of next semester	July 12, 2023	January 11, 2024
<b>Note: Final Practical Exam will be conducted by the concern department.</b>		

  
 Professor Dr. S. M. Mahbub Ul Haque Majumder  
 Pro-Vice Chancellor  
 Daffodil International University

M.V. Saqib  11/09/23

  
 Registration for n/a

\*\* The date may be altered or revised as needed.

**Daffodil International University**  
Department of CSE

<b>Batch</b>	L-1, T-1 (Batch-65) (New Curriculum-3, Effective from Spring 2023)																			
<b>Previous Section</b>	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R		
<b>Renaming Section</b>	65_ A	65_ B	65_ C	65_ D	65_ E	65_ F	65_ G	65_ H	65_ I	65_ J	65_ K	65_ L	65_ M	65_ N	65_ O	65_ P	65_ Q	65_ R		

<b>Batch</b>	L-1, T2 (Batch-64) (New Curriculum-3, Effective from Spring 2023)																				
<b>Previous Section</b>	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	
<b>Renaming Section</b>	64_ A	64_ B	64_ C	64_ D	64_ E	64_ F	64_ G	64_ H	64_ I	64_ J	64_ K	64_ L	64_ M	64_ N	64_ O	64_ P	64_ Q	64_ R	64_ S	64_ T	

<b>Batch</b>	L-2, T1 (Batch-63) (New Curriculum-2, Effective from Spring 2022)																			
<b>Previous Section</b>	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R		
<b>Renaming Section</b>	63_ A	63_ B	63_ C	63_ D	63_ E	63_ F	63_ G	63_ H	63_ I	63_ J	63_ K	63_ L	63_ M	63_ N	63_ O	63_ P	63_ Q	63_ R		

<b>Batch</b>	L-2, T2 (Batch-62) (New Curriculum-2, Effective from Spring 2022)																			
<b>Previous Section</b>	A	B	C	D	E	F	G	H	I	J										
<b>Renaming Section</b>	62_ A	62_ B	62_ C	62_ D	62_ E	62_ F	62_ G	62_ H	62_ I	62_ J										

<b>Batch</b>	L-2, T2 (Batch-61) (New Curriculum-2, Effective from Spring 2022)																		
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<b>Renaming Section</b>	55_ A	55_ B	55_ C	55_ D	55_ E	55_ F	55_ G	55_ H	55_ I	55_ J	55_ K	55_ L	55_ M	55_ N	55_ O	55_ P	55_ Q	55_ R	55_ S	55_ T	
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Previous Section	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	U	V	W	X
Renaming Section	61_ A	61_ B	61_ C	61_ D	61_ E	61_ F	61_ G	61_ H	61_ I	61_ J	61_ K	61_ L	61_ M	61_ N	61_ O	61_ P	61_ Q	61_ R	61_ S	61_ T	61_ U	61_ V	61_ W	61_ X
Batch	L-3,T-1 (Batch-60) (New Curriculum-1, Effective from Spring 2019)																							
Previous Section	A	PCA	PCB	PC-C	PCD	PC-E																		
Renaming Section	60_ A	60_ B	60_ C	60_ D	60_ E	60_ F																		
Batch	L-3,T2 (Batch-59) ( New Curriculum-1, Effective from Spring 2019)																							
Previous Section	PCA	PCB	PC-C	A																				
Renaming Section	59_ A	59_ B	59_ C	59_ D																				
Batch	L-3,T2 (Batch-58) ( New Curriculum-1, Effective from Spring 2019)																							
Previous Section	A	B	PCA	PCB	PC-C																			
Renaming Section	58_ A	58_ B	58_ C	58_ D	58_ E																			
Batch	L-4,T1 (Batch-57) ( New Curriculum-1, Effective from Spring 2019)																							

<b>Previous Section</b>	A	B	PCA	PCB																	
<b>Renaming Section</b>	57_ A	57_ B	57_ C	57_ D																	
<b>Batch</b>	L-4,T2 (Batch-56) ( New Curriculum-1, Effective from Spring 2019)																				
<b>Previous Section</b>	PCA	PCB	PC-C	A	B	C															
<b>Renaming Section</b>	56_ A	56_ B	56_ C	56_ D	56_ E	56_ F															
<b>Batch</b>	L-4,T2 (Batch-55) ( New Curriculum-1, Effective from Spring 2019)																				
<b>Previous Section</b>	A	B	C	D	E	F	G	H	I	PCA	PCB	PC-C	PC-D	PC-E	PC-F	PCH	PC-I	PC-K	PC-L	PCR	



**Daffodil International University**  
**Department of Computer Science and Engineering**  
**Tentative Midterm Examination Routine, fall 2023**

Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_1	ENG-101 Basic Functional English and English Spoken [900]	L-1, T-1 (Batch-65)	CSE231 Algorithm [1200]	L-2, T-2 (Batch-61)	CSE-121 Electrical Circuits [1000]	L-1, T-2 (Batch-64)
	CSE225 Data Communication [500]	L-2, T-2 (Batch-62)			CSE423 Information Security [300]	L-4, T-2 (Batch-56)
Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_2	CSE323 Operating Systems [200]	L-3, T-2 (Batch-59)	CSE331 Compiler Design [250]	L-3, T-2 (Batch-58)	CSE231 Microprocessor, Embedded Systems and IoT [300]	L-3, T-1 (Batch-60)
	CSE212 Basic Electronics [900]	L-2, T-1 (Batch-63)	CSE412 Big Data and IoT [1000]	L-4, T-2 (Batch-55)	CSE412 Big Data and IoT [200]	L-4, T-1 (Batch-57)
Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_3	ENG-102 Writing and Comprehension [1000]	L-1, T-2 (Batch-64)	CSE-113 Programming and Problem Solving [900]	L-1, T-1 (Batch-65)	STA227 Statistics and Probability [1200]	L-2, T-2 (Batch-61)
			STA227 Statistics and Probability [500]	L-2, T-2 (Batch-62)		
Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_4	MAT211 Engineering Mathematics [900]	L-2, T-1 (Batch-63)	CSE333 Software Engineering [250+200]	L-3, T-2 (Batch-58) + L-4, T-1 (Batch-57)		
	CSE315 Artificial Intelligence [300+200]	L-3, T-1 (Batch-60) +	CSE498 Social and Professional Issues in Computing [1000+300]	L-4, T-2 (Batch-55) + L-4, T-2 (Batch-56)		

		L-3,T-2 (Batch-59)				
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Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_5	CSE-112 Computer Fundamentals [900]	L-1, T-1 (Batch-65)	CSE134 Data Structure [1200]	L-2, T-2 (Batch-61)	CSE-123 Data Structure [1000]	L-1, T-2 (Batch-64)
Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_6	CSE214 Object Oriented Programming [900]	L-2, T-1 (Batch-63)	CSE214 Object Oriented Programming [500]	L-2, T-2 (Batch-62)	CSE325 System Analysis and Design [200+250]	L-3,T-2 (Batch-59) + L-3,T-2 (Batch-58)
	CSE 445 Natural Language Processing [200+300]	L-4,T-1 (Batch-57) + L-4,T-2 (Batch-56)	CSE427 Digital Image Processing [1000]	L-4,T-2 (Batch-55)	CSE311 Database Management System [300]	L-3,T-1 (Batch-60)
Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_7	CSE235 Numerical Methods [1200]	L-2, T-2 (Batch-61)	CSE-115 Introduction to Biology and Chemistry for Computation [900]	L-1, T-1 (Batch-65)	MAT-102 Mathematics-II: Calculus, Complex Variables and Linear Algebra [1000]	L-1, T-2 (Batch-64)
Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_8	GED216 History of Bangladesh and Bangla Language [900]	L-2, T-1 (Batch-63)	GED216 History of Bangladesh and Bangla Language [500]	L-2, T-2 (Batch-62)	CSE321 Data Mining and Machine Learning [200+250]	L-3,T-2 (Batch-59) + L-3,T-2 (Batch-58)
	CSE414 Web Engineering [200]	L-4,T-1 (Batch-57)	CSE426 Principles of Robotics [1000]	L-4,T-2 (Batch-55)	CSE313 Computer Networks [300]	L-3,T-1 (Batch-60)
Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_9	PHY- 102 Physics - II [1000]	L-1, T-2 (Batch-64)	PHY-101 Physics-I [900]	L-1, T-1 (Batch-65)		
Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_10	ACT322 Financial and Managerial Accounting [900]	L-2, T-1 (Batch-63)	CSE334 Pervasive Computing [250]	L-3,T-2 (Batch-58)		
	CSE421 Computer Graphics [200]	L-4,T-1 (Batch-57)	CSE223 Digital Electronics [500]	L-2, T-2 (Batch-62)		
			CSE236			

Math for Computer Science  
[300]

L-3,T-1 (Batch-60)

Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_11			MAT-101 Mathematics - I [900]	L-1, T-1 (Batch-65)		
Date	Slot A: 9.00 – 10.30 am	Batch	Slot B: 11.30 – 1.00 pm	Batch	Slot C: 02.00 – 03.30 pm	Batch
Day_12	ECO237 Economics [900]	L-2, T-1 (Batch-63)	ECO237 Economics [1200]	L-2, T-2 (Batch-61)		
	ECO321 Economics [200]	L-3,T-2 (Batch-59)				

### Instructions for Students

1. No student will be allowed to sit for examination without clearing their payment to the university or Showing proper document.
2. Each student is instructed to collect his/her Admit Card or Payment Document from Accounts Section and bring it to examination hall. No student will be allowed to sit for Examination without Admit Card.
3. It is mandatory to attend the examination according to the sitting arrangement; if not, students will be strictly redirected to follow the sitting arrangement.
4. Students are strongly advised to write their teachers' initials (for respective courses & sections) correctly to avoid inconvenient circumstances.
5. ID card is mandatory to enter into exam hall.
6. Hang your student ID card with DIU Ribbon with clear visibility.

Dewan Mamun Raza (Convener)  
Lamia Rukhsara (Member)  
Md. Abbas Ali Khan (Member)  
Firoz Hasan (Member)  
Mushfiqur Rahman (Member)  
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Examination Committee, FSIT

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